I CLAIM:

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1. A lamp actuating facility comprising:

a plurality of lamps each including an output terminal, an inverter circuit coupled to said lamps, to convert electric power and to energize said lamps,

a low frequency control unit coupled to said inverter circuit, to set an average current value at said output terminals of said lamps, and to control said inverter circuit,

a plurality of current detecting units coupled between said lamps and said low frequency control unit respectively, to obtain the average current value at said output terminals of said lamps, and to send the average current value back to said low frequency control unit, and

a plurality of regulating devices coupled between said lamps and said low frequency control unit respectively, to control electric power through said lamps, and to maintain each of said lamps at the average current value.

- 2. The lamp actuating facility as claimed in claim 1 further comprising a MOSFET coupled between said inverter circuit and said low frequency control unit.
- 3. The lamp actuating facility as claimed in claim 2, wherein said MOSFET includes a drain electrode coupled to said inverter circuit via an inductor, and a gate electrode and a source electrode grounded.
- 4. The lamp actuating facility as claimed in claim 1, wherein each of said regulating devices includes a first transistor having a base coupled to said low frequency control unit, a collector coupled

to electric power source, and an emitter grounded.

- 5. The lamp actuating facility as claimed in claim 4, wherein each of said regulating devices further includes a first resistor having two ends, a second resistor, a second transistor having a base coupled to said collector of said first transistor, and having a collector and an emitter coupled to said ends of said first resistor, and then grounded via said second resistor.
- 6. The lamp actuating facility as claimed in claim 5, wherein said first resistor includes a resistance greater than that of said second resistor.
- 7. The lamp actuating facility as claimed in claim 1, wherein each of said current detecting units includes an integrator circuit coupled to said low frequency control unit.

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